

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1		computer components	1	CR		
2		Computer function inter connection structure	1	CR		
3		Bus inter connection arithmetic & logic unit	1	CR		
4		Integer representation	1	CR		
5		Integer Arithmetic	1	CR		
6						
7		Fixed point & floating point representation	1	CR		
8						
9		central processing unit Instruction set	11	CR		
10		machine instruction characteristics	11	CR		
11		Addressing modes	11	CR		
12						
13		Instruction format	11	CR		
14		Assembly language	11	CR		
15		Register organization	11	CR		
16		Instruction cycle	11	CR		
17		Instruction pipelining	11	CR		
18						
19		Control of the processor	111	CR		
20		Hardwired implementation	111	CR		
21		microprogrammed control	111	CR		
22		micro instruction sequencing	111	CR		

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23 24		Integer Multiplication & Division	III	CR		
25		Integer Addition Addition, Subtraction	IV	CR		
26 27		Multiplication Algorithms	IV	CR		
28 29		Division Algorithms	IV	CR		
31		Floating Point Arithmetic Operations	IV	CR		
32		Decimal Arithmetic Unit	IV	CR		
33		Decimal Arithmetic Operations	IV	CR		
34		Memory System Main Memory	V	CR		
35		Auxiliary memory	V	CR		
36		Addressable memory	V	CR		
37 38		CACHE memory Organization	V	CR		
39 40		Virtual memory	V	CR		
41 42		memory management hardware	V	CR		
43		Input output Organization	VI	CR		
44		Input output Controller	VI	CR		
45 46		modes of data transfer	VI	CR		
47		Direct memory Access	VI	CR		

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[illegible]